

10/587646

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AP20 Rec'd PCT/PTO 27 JUL 2006

Appl. No. : Unassigned Confirmation No. Unassigned  
First Inventor : VAN UDEN, Albertus, J., P., M.  
Filed : Concurrently  
TC/A.U. : Unassigned  
Examiner : Unassigned

Docket No. : NL040064US1  
Customer No. : 24738

Title: Integrated Circuit Device with a ROM Matrix

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

Prior to calculation of the filing fee and examination please amend the above-identified application as follows:

**Amendments to the Specification** there are no amendments in this paper.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.